

07/08/00
J0687 U.S. PTO

07-11-00
A
Comparable to Form PTO/SB/05 (12/97)
Approved for use through 09/30/00. OMB0651-0032
Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

UTILITY PATENT APPLICATION TRANSMITTAL
(For new Non-provisional applications under 37 CFR1.53(b))

09/16/99
S. H. 158-0
j-c-87
00/00/00

Attorney Docket No.: SEM4492P1360US
First Named Inventor: Thomas L. Ritzdorf
Express Mail Label No. EL601557794US

Box PATENT APPLICATION
Hon. Commissioner For Patents
Washington, D.C. 20231

Sir:

Transmitted herewith for filing is a new utility patent application of inventor(s): **Thomas L. Ritzdorf, Steve L. Eudy, Gregory J. Wilson, Paul R. McHugh** and entitled: **Apparatus and Method For Processing A Microelectronic Workpiece Using Metrology**.

Application Elements:

1. ☒ Specification containing 48 pages (preferred arrangement set forth below)
- Descriptive Title of the Invention
 - Cross-reference to related applications (if applicable)
 - Statement regarding Federally-sponsored Research & Development (if applicable)
 - Reference to Microfiche Appendix (if applicable);
 - Background of the Invention
 - Brief Summary of the Invention
 - Brief Description of the Drawings (if filed)
 - Detailed Description
 - Claim(s)
 - Abstract of the Disclosure
2. ☒ Drawings: 10 Sheets of ☐ formal drawings ☒ informal drawings
3. ☒ Oath or Declaration
- a. ☐ An executed declaration or oath for the utility patent application including a power of attorney;
 - b. ☒ An unexecuted declaration or oath for the utility patent application including a power of attorney;
 - c. ☐ Copy from a prior application (37 CFR 1.63(d), for continuation/divisional with No. 16 completed. [Note No. 4 below.])
 - i. ☐ Signed statement attached deleting inventor(s) named in the prior application (see 37 CFR 1.63(d)(2) and 1.33(b)).
4. ☐ **For CONTINUATION or DIVISIONAL Applications only:** The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 3c, is considered as being part of the disclosure of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.

5. ☐ Microfiche Computer Program (Appendix)
6. ☐ Nucleotide and/or Amino Acid Sequence submission. including:
☐ Computer readable copy,
☐ Paper copy (identical to computer copy),
☐ Statement verifying identity of above copies.

Accompanying Application Parts:

7. ☐ Assignment Papers (cover sheet, document(s) and requisite fee).
8. ☐ 37 CFR 3.73(b) Statement (where there is an assignee)
☐ Power of Attorney
9. ☐ English Translation document (if applicable)
10. ☐ Information Disclosure Statement (IDS), including PTO-1449
☐ Copies of IDS Citations
11. ☐ Preliminary Amendment
12. ☒ Return Postcard for PTO Mail Room Date Stamp (should be specifically itemized).
13. ☐ Small Entity Statement(s)
☐ Statement filed in prior application, status still proper and desired.
14. ☐ Certified Copy of Priority Document(s) (if foreign priority is claimed).
15. ☐ Other _____
16. ☐ **If Continuing Application**, check appropriate box and supply the requisite information below and in a preliminary amendment:
☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No. _____, filed _____.
Prior application information: Examiner: _____ Group/Art Unit: _____

Fee Calculation


The filing fee has been calculated as shown below:

Small Entity						Large Entity	
For	No. Filed	No. Allowed	No. Extra	Rate	Fee	Rate	Fee
Basic Fee					\$345.00		\$690.00
Total Claims	26	- 20 =	6	x \$9.00	\$	x \$18.00	\$108.00
Indep. Claims	4	- 3 =	1	x \$39.00	\$	x \$78.00	\$78.00
Multiple Dependent Claims Present				+ \$130.00	\$	+ \$260.00	\$
				TOTAL	\$	TOTAL	\$876.00

17. ☐ A check in the amount of \$_____ to cover the filing fee is enclosed.
18. ☐ Please charge my Deposit Account No. 04-1644 in the amount of \$_____.
19. ☐ The Commissioner is authorized to charge payment of the following amounts associated with this communication or credit any overpayment to Deposit Account No. 04-1644:
- ☐ Additional filing fees under 37 CFR 1.16 or deficiencies in remittances therefor.
 - ☐ Additional processing fees under 37 CFR 1.17 or deficiencies in remittances therefor.
 - ☐ **ONLY if applicant has partially paid** the patent issue fee under 37 C.F.R. §1.18, then the **deficiency** shall be charged to Deposit Account No. 04-1644, and the Commissioner is authorized to so charge the Deposit Account.

Date: July 8, 2000

Attorney's Signature _____


Randall T. Erickson, Reg. No. 33872

Correspondence Address:

ROCKEY, MILNAMOW & KATZ, LTD.

Two Prudential Plaza

180 North Stetson Avenue, Suite 4700

Chicago, Illinois 60601

Telephone: (312) 616-5400

Facsimile: (312) 616-5460

CERTIFICATE OF MAILING BY EXPRESS MAIL

I hereby certify that this Utility Patent Application Transmittal, enclosed application, and any other documents referred to as enclosed herein, are being deposited in an envelope with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated below and addressed to Box PATENT APPLICATION, Assistant Commissioner for Patents, Washington, D.C. 20231.

Express Mail Label No.: EL601557794US

Date of Deposit: July 8, 2000

Typed/Printed Name of Person Signing: Randall T. Erickson

Signature: 

CERTIFICATE OF MAILING BY EXPRESS MAIL

"Express Mail" Mailing Label Number EL 601 557 794 US

Date of Deposit: July 8, 2000

I hereby certify that this correspondence is being deposited with the United States Postal "Express Mail Post Office to Addressee" service under 37 CFR

1.10(c) on the date indicated above and is addressed to:

**Box PATENT APPLICATION
Commissioner for Patents
Washington, D.C. 20231**

Case Number: SEM4492P1360US

Serial No. None

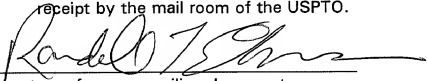
Filing Date: July 8, 2000

Inventor: Thomas L. Ritzdorf et al

For: "Apparatus and Method for Processing A Microelectronic Workpiece Using Metrology"

Enclosed are the following documents:

Certificate of Mailing by Express Mail; Utility Patent Application Transmittal (3 pages), in duplicate, with no Check No. for \$ attached; and the application consisting of 39 pages of specification; 8 pages of claims comprising 26 claims; 1 page of abstract; 10 sheets of informal drawings; and an unexecuted Declaration and Power of Attorney consisting of 3 pages; and a prepaid post card for acknowledgment of receipt by the mail room of the USPTO.


Signature of person mailing documents

Attorney Docket No. SEM4492P1360US
Corporate Docket No. P00-0000

TITLE OF THE INVENTION

APPARATUS AND METHOD FOR PROCESSING A MICROELECTRONIC
WORKPIECE USING METROLOGY

CROSS-REFERENCE TO RELATED APPLICATIONS
Not Applicable

STATEMENT REGARDING FEDERALLY
SPONSORED RESEARCH OR DEVELOPMENT

Not Applicable

00612175-070800

BACKGROUND OF THE INVENTION

The present invention is directed to an apparatus and method for processing a microelectronic workpiece. More particularly, the present invention is directed to an improved apparatus and method of processing a microelectronic workpiece using a metrology result representative of a microelectronic workpiece condition. For purposes of the present application, a microelectronic workpiece is defined to include a microelectronic workpiece formed from a substrate upon which microelectronic circuits or components, data storage elements or layers, and/or micro-mechanical elements are formed.

The fabrication of microelectronic components from a microelectronic workpiece, such as a semiconductor wafer substrate, polymer or ceramic substrate, etc., involves a substantial number of operations performed on the microelectronic workpiece. Such operations include, for example, material deposition, patterning, doping, chemical mechanical polishing, electropolishing, and heat treatment.

Material deposition processing involves depositing or otherwise forming thin layers of material on the surface of the microelectronic workpiece. Patterning provides deposition or removal of selected portions of these added layers. Doping of a microelectronic workpiece such as a the semiconductor wafer, is the process of adding impurities known as "dopants" to the selected portions of the microelectronic workpiece to alter the electrical characteristics of the substrate material. Heat treatment of the microelectronic workpiece involves heating and/or cooling the

microelectronic workpiece to achieve specific process results. Chemical mechanical polishing involves the removal of material through a combined chemical/mechanical process, while electropolishing involves the removal of material from a microelectronic workpiece surface using electrochemical reactions.

Production of semiconductor integrated circuits and other microelectronic devices from microelectronic workpieces, such as semiconductor wafers, typically requires the formation and/or electrochemical processing of one or more thin film layers on the microelectronic workpiece. The microelectronic manufacturing industry has applied a wide range of thin film layer materials to form such microelectronic structures. These thin film materials include metals and metal alloys such as, for example, nickel, tungsten, tantalum, solder, platinum, copper, aluminum, gold, etc., as well as dielectric materials, such as metal oxides, semiconductor oxides, and perovskite materials.

Electroplating and other electrochemical processes, such as electropolishing, electro-etching, anodization, etc., have become important in the production of semiconductor integrated circuits and other microelectronic devices from such microelectronic workpieces. For example, electroplating is often used in the formation of one or more metal layers on the microelectronic workpiece. These metal layers are typically used to electrically interconnect the various devices of the integrated circuit. Further, the structures formed from the metal layers may constitute microelectronic devices such as read/write heads, etc.

Electroplated metals typically include copper, nickel, gold, platinum, solder, nickel-iron, etc. Electroplating is generally effected by initial formation of a seed layer on the microelectronic workpiece in the form of a very thin layer of metal, whereby the surface of the microelectronic workpiece is rendered electrically conductive. This electro-conductivity permits subsequent formation of a blanket or patterned layer of the desired metal by electroplating. Subsequent processing, such as chemical mechanical planarization, may be used to remove unwanted portions of the patterned or metal blanket layer formed during electroplating, resulting in the formation of the desired metallized structure.

Electropolishing of metals at the surface of a microelectronic workpiece involves the removal of at least some of the metal using an electrochemical process. The electrochemical process is effectively the reverse of the electroplating reaction and is often carried out using the same or similar reactors as electroplating.

Anodization typically involves oxidizing a thin-film layer at the surface of the microelectronic workpiece. For example, it may be desirable to selectively oxidize certain portions of a metal layer, such as a Cu layer, to facilitate subsequent removal of the selected portions in a solution that matches the oxidized material faster than the non-oxidized material. Further, anodization may be used to deposit certain materials, such as perovskite materials, onto the surface of the microelectronic workpiece.

As the size of various microelectronic circuits and components decreases, there is a corresponding decrease in the manufacturing tolerances that must be met by the manufacturing tools. It is desirable that electrochemical processes uniformly process the surface of a given microelectronic workpiece. It is also desirable that the electrochemical process meet microelectronic workpiece-to-microelectronic workpiece uniformity requirements.

Multiple processes must be executed upon a microelectronic workpiece to manufacture the desired microelectronic circuits, devices, or components. These processes are generally executed in processing tools that are specifically designed to implement one or more of the requisite processes. In order to automate the processing and minimize operator handling, tool architectures have been developed that incorporate multiple processing stations and automated transfer of the microelectronic workpieces from one processing station to the next.

In such tools, the microelectronic workpieces are processed individually at the various processing stations. Furthermore, multiple microelectronic workpieces are concurrently processed at different processing stations. Thus, one microelectronic workpiece may be processed in one of the processing stations while another microelectronic workpiece is concurrently processed in another one of the processing stations. In this way, a pipeline processing approach can be developed, which enhances production throughput. Additionally, processing steps that take

longer to perform may have multiple processing stations devoted to performing that particular processing step, thereby enhancing production throughput.

Numerous processing tools have been developed to implement the foregoing processing operations. These tools take on different configurations depending on the type of microelectronic workpiece used in the fabrication process and the process or processes executed by the tool. An exemplary tool embodiment is disclosed in U.S. Patent Application Serial No. 08/991,062, filed December 15, 1997, entitled "Semiconductor Processing Apparatus Having Lift and Tilt Mechanism."

One tool configuration, known as the LT-210C™ processing tool and available from Semitool, Inc., of Kalispell, Montana, includes a plurality of microelectronic workpiece processing stations such as one or more rinsing/drying stations, one or more wet processing stations, and one or more thermal processing stations that includes a rapid thermal processing ("RTP") reactor. Such wet processing operations include electroplating, etching, cleaning, electroless deposition, electropolishing, etc..

In the processing of microelectronic workpieces, the output of one process is the input for the next process, and such output typically influences the output of the next process. This is true, for instance, in the case of a copper damascene interconnect process, with the barrier/seed layer process output influencing the output of the copper electrochemical deposition ("ECD") process, or the output of the copper ECD process influences the output of the copper chemical mechanical

polishing ("CMP") process. This is also the case in most thin film ECD processes, where the thickness and the thickness uniformity of the seed layer affect the thickness uniformity of the plated film.

The present inventors have recognized the desirability of, in a process having a sequence of steps, automatically adjusting a workpiece processing step to effect its output in response to the output of a prior step or a subsequent step.

SUMMARY OF THE INVENTION

The present invention provides an apparatus and method for processing a microelectronic workpiece, using a metrology measurement of a microelectronic characteristic, such as seed layer thickness or uniformity, measured on a microelectronic workpiece, to influence or control the process. The metrology measurement can be taken subsequent to a prior processing step, i.e., a feed forward control, or subsequent to a process being controlled, i.e., a feed back control. The metrology measurement can be taken on each microelectronic workpiece to be processed, or on a first microelectronic workpiece, or a sample microelectronic workpiece, for a batch of microelectronic workpieces. In general, the invention is useful in situations where a process output affects the output of a subsequent process output in a known manner, or in a manner that can be empirically determined.

When a relationship between a first process output and a subsequent, second process output as described above exists, the second process can be modified in a manner determined by the output of the first process, in order to ensure that the output from the second process is as desired (e.g. as uniform and repeatable as possible), regardless of variation in the output of the first process. The desired output could be different than merely trying to produce uniform results, however; for example, it is possible that intentional variation in one parameter (e.g. film thickness) could be introduced in order to compensate for another non-uniformity (e.g. line

width) to produce uniform electrical results. Furthermore, a measurement of the output of the first process can be incorporated into the apparatus that performs the second process, and the data from this measurement can be used as an input to a mathematical algorithm that is used to tune the second process.

The apparatus of the invention can include a control that modifies the process parameters of a process in order to compensate for material variations in the incoming microelectronic workpiece, in order to produce a uniform output or desired output from the process. The material variations in the microelectronic workpiece fed to this second process could be due to variability in a prior process step or to the use of different operations or processing chambers to feed the process. The apparatus of the invention can include an in-line metrology measurement system to determine the condition of the incoming microelectronic workpiece material, and a control for altering the process conditions based on the measurement results, i.e., a feed forward control. The metrology system may additionally be used to measure the output of the process as well. Alternately, the metrology system can measure the output of the process and the control can alter the process conditions of subsequently processed microelectronic workpieces, i.e., a feed back control.

According to one exemplary aspect of the invention, metrology integration and ECD seed layer integration are utilized. The metrology integration, either physical or virtual through a network link, allows dynamic control of the process. The ECD seed layer integration allows clustered processing which lowers costs and

facilitates “split lot” processing, i.e., differing process recipes, for two or more groups of workpieces within a batch.

The invention can be advantageously configured in a high volume manufacturing configuration or a process development configuration.

According to the high volume configuration, such as for an ECD tool, the tool preserves high volume ECD capability while also adding a “repair or recovery” mode to maintain the finished plating integrity. Under normal operation, the tool may be used with or without periodic verification through in-line metrology.

The metrology system can be used to measure the first workpiece of a lot, or to measure from a specific process location of the prior step (e.g., a given chamber on a seed layer sputtering tool) to verify good incoming quality of seed layers or other parameters. Likewise, the metrology system can feed forward or feed back uniformity and thickness data to drive the process recipe for electroplating reactors.

The metrology system of the invention is particularly useful in the case of reactors having the advantageous ability to manipulate wafer uniformity through process recipe control. The reactors can be adjusted to varied electrochemical processing requirements, such as in response to metrology data, to provide a controlled, substantially uniform diffusion layer and electrical potential at the surface of the microelectronic workpiece that assists in providing a corresponding substantially uniform processing of the microelectronic workpiece surface (e.g., uniform deposition of the electroplated material). Such electrochemical processing

techniques can be used in the deposition and/or alteration of blanket metal layers, blanket dielectric layers, patterned metal layers, and patterned dielectric layers.

The process and apparatus can be controlled with increased versatility when using the metrology data. Based upon the output from the metrology unit, the user can decide to stop the subsequent process to resolve the issues driving the prior process. For example, an electroplating process can be stopped when seed layer thicknesses are below acceptable tolerances. Alternately, the user can continue the subsequent processing and adjust the subsequent process steps or process parameter based upon the output from the metrology unit. For example, where seed layer thickness or uniformity is unacceptable, the user can insert an intermediate step and automatically "fix" a seed layer problem with a seed layer enhancement process, such as an electrochemical deposition (ECD) seed layer enhancement process. The user can also continue the processing and automatically adjust the process recipe on ECD reactors to achieve acceptable plating uniformity and thickness. Also, rather than attempt to fix or compensate for a seed layer non-uniformity, a rejected workpiece can be recovered in a non-compliance station, or sent first to a stripping unit to have the nonconforming layer removed and then sent to the non-compliance station. Microelectronic workpieces stored in the non-compliance station can be removed from the apparatus for recovery (reuse).

Furthermore, the apparatus of the invention is easily configured for high volume manufacturing with ECD seed layer enhancement integrated as part of the

standard process, irrespective of the presence of seed layer non-uniformity. The number of ECD seed layer chambers can correlate to the throughput requirement. As dual damascene features continue to become more aggressive, the capability of physical vapor deposition ("PVD") to conformably deposit the requisite seed layer in these features becomes limited. The ECD seed layer is a promising approach to extend ECD processes beyond the limits of current PVD technology.

An alternate exemplary embodiment of the tool incorporating the present invention is a process development configuration. This tool design is directed to developing optimized processes. The configuration allows a wide range of flexibility in process sequence and control. For example, a process engineer might want to measure incoming seed layer thickness, ECD seed layer deposition results, ECD fill results, and post annealing results. Since the plating solution reservoirs can be much smaller, the user may also quickly and easily interchange chemistries for rapid and low-cost experimentation. The user may want to run split lots with a wide variety of process combinations to determine feasibility of a production process.

The invention can be applied to a chemical mechanical polishing ("CMP") tool. For example, metrology can be used to adjust the polishing recipe of the CMP tool based on feed forward data on incoming film thickness, or feed back data from a workpiece measured subsequent to the CMP process.

A further alternate embodiment of the invention is an integrated metrology system for a multi-tool apparatus. The apparatus can, for example, include a seed

layer tool, an electrochemical deposition tool, and a chemical mechanical polishing tool. At least one metrology unit is utilized which communicates with a control to influence process recipes or process step sequence on one or more of the tools. Each tool can have a dedicated metrology unit to measure incoming material or output results. The metrology results or data can be fed forward or fed back to the respective other tools to control or otherwise influence the process of the other tools.

Numerous other advantages and features of the present invention will become readily apparent from the following detailed description of the invention and the embodiments thereof, from the claims and from the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

FIGURE 1 is an exploded isometric view of a prior art processing tool;

FIGURE 2 is a schematic plan view of a microelectronic workpiece processing apparatus of the present invention;

FIGURE 3 is a schematic plan view of a first embodiment of the present invention;

FIGURE 4 is a schematic plan view of a second embodiment of the invention;

FIGURE 5 is a block diagram of a sequence of processing steps in accordance with a first method of the present invention;

FIGURE 6 is a block diagram of a sequence of processing steps in accordance with a second method of the present invention;

FIGURE 7 is a block diagram of a sequence of processing steps in accordance with a third method of the present invention;

FIGURE 8 is a block diagram of a sequence of processing steps in accordance with a fourth method of the present invention;

FIGURE 9 is a schematic plan view of an alternate embodiment microelectronic workpiece processing apparatus of the present invention; and

FIGURE 10 is a schematic plan view of a further alternate embodiment microelectronic workpiece processing apparatus of the present invention.

DETAILED DESCRIPTION OF THE INVENTION

While this invention is susceptible of embodiment in many different forms, there are shown in the drawings and will be described herein in detail, specific embodiments thereof with the understanding that the present disclosure is to be considered as an exemplification of the principles of the invention and is not intended to limit the invention to the specific embodiments illustrated.

SYSTEMS

Integrated Processing Tool

FIG. 1 is an exploded isometric view of a prior art integrated microelectronic workpiece-processing tool 10. This exemplary tool embodiment is disclosed in U.S. Patent Application Serial No. 08/991,062, filed December 15, 1997, entitled "Semiconductor Processing Apparatus Having Lift and Tilt Mechanism."

Although modularity is not necessary to the overall tool function, the tool 10 is shown as having been separated into individual modular components. The exemplary integrated microelectronic workpiece processing tool 10 of FIG. 1 comprises an input/output section 20, a processing section including first and second processing subsections 30 and 40, a microelectronic workpiece transfer apparatus 50, an exhaust assembly 60, and an end panel 70.

The input/output section 20 includes an opening 80 through which one or more cassettes can be received or removed. Generally stated, cassettes that are received

at the input/output section 20 include microelectronic workpieces that are to be processed within the tool 10, while cassettes that are removed from the input/output section 20 include microelectronic workpieces that have already been processed within the tool 10. However, it will be recognized that a processed microelectronic workpiece may be returned directly to the cassette from which it was respectively provided to the tool.

In the embodiment of FIG. 1, the cassettes are received directly by one or more direct-access assemblies that, in turn, allow direct access to individual microelectronic workpiece slots of the cassettes. For example, in the specific tool shown here, the cassettes are directly received by and removed from one or more direct-access assemblies. The direct-access assemblies of the illustrated embodiment are constructed as lift/tilt assemblies that both lift the cassette and reorient it for presentation to a subsequent microelectronic workpiece transfer assembly. When the lift/tilt assemblies initially receive the cassettes, the microelectronic workpieces are in a first position with respect to horizontal, such as a substantially vertical position. Each lift/tilt assembly then reorients (i.e. tilts) the respective cassette to a second position with respect to horizontal, such as a microelectronic workpiece horizontal position. Each lift/tilt assembly is used to position the respective microelectronic workpiece cassettes to an orientation in which the microelectronic workpiece holding positions, such as microelectronic workpiece slot positions, of the cassette are individually accessible. While oriented in this second position, the microelectronic workpiece slots

and corresponding microelectronic workpiece, if any, of each cassette are therefore generally accessible to the microelectronic workpiece transfer apparatus 50. In the illustrated tool, microelectronic workpiece transfer apparatus 50 includes one or more microelectronic workpiece transport units 90 and 100. The microelectronic workpiece transport units 90 and 100 may be used to transport individual microelectronic workpieces along the conveyor path 110, between the cassettes and one or more processing stations 120 of processing subsections 30 and 40 and, further, may be used to transport microelectronic workpieces between individual processing stations 120. The various sections of the integrated microelectronic workpiece processing tool 10 may define an enclosed space that is generally separate from the external environment. To this end, exhaust assembly 60 enables venting of airborne contaminants initially present or produced during processing of the microelectronic workpieces to thereby generate and/or maintain a relatively clean processing environment within the enclosed space.

After the microelectronic workpieces are processed, the transfer apparatus 50 places the microelectronic workpieces into a cassette, and the cassette containing the processed microelectronic workpieces are removed from the integrated microelectronic workpiece-processing tool 10 via the opening 80 in the input/output section 20.

Metrology Controlled Processing Tool

FIGURE 2 illustrates in schematic fashion a processing tool 200 of the present invention, which is similar to the tool shown in Figure 1 except as noted. The tool 200 includes an input/output station 224 at one end, a linear conveyor arrangement or linear robot 226 extending from the input/output station along a length of the tool 200, and a number of processing stations. The processing stations can include a metrology unit 228, one or more ECD seed layer enhancement units 232, one or more stripping units 236, and one or more plating units 240. Additionally, the tool 200 includes one or more annealing units 244 and a non-process station or staging station 248.

The linear robot 226 includes a rail 250 (Figure 3) which extends substantially the length of the processing units, and which carries a robot arm manipulator or transport unit 256 thereon. The robot arm manipulator 256 can remove a wafer from the input/output station 224 and deliver the microelectronic workpiece to and from any of the processing units 232, 236, 240, 244 or to and from the metrology unit 228 and to and from the non-process station 248.

In one mode of operation, the in-film metrology unit 228 measures a seed layer thickness or uniformity on a workpiece and communicates the data to a controller 270. The controller can be a programmable controller. Based on the data, decisions concerning the process parameters or recipe downstream from the metrology unit are made. The process recipe for one or more downstream units can

be modified based on the metrology results. Alternatively, or additionally, the process sequence can be modified according to the metrology results. For example, if the seed layer thickness or uniformity is insufficient, or less than a tolerance value, the microelectronic workpiece can be delivered to one of the seed layer enhancement units 232 before being delivered to one of the electroplating units 240. Alternatively, if the seed layer is defective or has a thickness out of tolerance by an unacceptable amount, such that the seed layer cannot be repaired or enhanced in the seed layer enhancement unit 232, the microelectronic workpiece can be delivered to one of the stripping units 236 wherein the microelectronic workpiece can be etched, including its process side surface and beveled edge, to be thereafter delivered by the manipulator 256 to the non-process station 248. The non-process station can be a non-compliance station, including a cassette 248a for holding microelectronic workpieces for returning microelectronic workpieces to a seed layer application station, typically a physical vapor deposition (PVD) apparatus external to the described tool 200. After the microelectronic workpiece has been plated according to the process recipe in one of the electroplating units 240, it can be delivered to the in-line anneal unit for annealing, and thereafter delivered to the input/output station 224 for exporting to a next process tool.

High Throughput Embodiment

A high volume or high throughput tool 300 is illustrated in Figure 3. According to the high volume configuration, the tool preserves high volume ECD capability while also adding a "repair or recovery" mode to maintain the finished plate integrity. Under normal operation, the tool may be used with or without periodic verification through in-line metrology at the metrology unit 228.

The metrology unit can be used to measure the first substrate of a lot, or from a specific process location of the prior step (e.g., a given chamber on a seed layer sputtering tool) to verify good incoming quality of seed layers or other parameters. Likewise, the metrology unit can feed forward or feed back uniformity and thickness data to drive the process recipe for the electroplating reactors 240.

The electroplating units 240 are preferably adjustable reactors (described below) or other type reactors that can adapt to varied electrochemical processing requirements while concurrently providing a controlled, substantially uniform diffusion layer and electrical potential at the surface of the microelectronic workpiece that assists in providing a corresponding substantially uniform processing of the microelectronic workpiece surface (e.g., uniform deposition of the electroplated material). The electroplating units 240 can be controlled by the controller 270 (Figure 2) to compensate for non-uniformities of the seed layer determined by the metrology unit. Such electrochemical processing techniques can be used in the

deposition and/or alteration of blanket metal layers, blanket dielectric layers, patterned metal layers, and patterned dielectric layers.

The tool 300 can be controlled with increased flexibility when using the metrology unit. Based upon an output from the metrology unit 228 derived from the programmable recipe from the metrology unit 228, the user can decide to stop the subsequent microelectronic workpiece processing, such as the electroplating units 240, and resolve the issues driving the prior process, such as a seed layer deposition process. For example, the electroplating process can be stopped where seed layer thicknesses are below acceptable tolerances.

Alternately, the user can continue the subsequent processing and adjust the order of subsequent process steps, or insert a remedial process step, based upon the output from the metrology unit. For example, the user can first transport the wafer to a seed layer enhancement unit 232 to automatically "fix" or adjust a seed layer problem with the ECD seed layer enhancement process and then transport the microelectronic workpiece to an electroplating unit 240.

Rather than changing the order of the process steps or inserting an intermediate step, the user can also continue the processing and automatically adjust the process recipe in the electroplating unit 240, particularly using variable recipe reactors, for enhanced plating uniformity and thickness.

Still further, if a microelectronic workpiece seed layer is too far out of tolerance in thickness or uniformity, the microelectronic workpiece can be

transported to one of the stripping units 236 where the microelectronic workpiece processing side is stripped. The microelectronic workpiece can then be transported to the non-compliance station 248, particularly to the cassette 248a, for recycling.

The tool 300 is also easily configured for high volume manufacturing with ECD seed layer enhancement integrated as part of the standard process, i.e., the number of ECD seed layer enhancement chambers 232 can correlate with the throughput requirement.

The stripping units 236 can also be used to clean copper contamination from the prior PVD seed layer process from the microelectronic workpiece back, edge and bevel to eliminate problems during chemical mechanical polishing (CMP).

The tool 300 can also include a microelectronic workpiece pre-aligner (not shown). The pre-aligner is described in "Semiconductor Processing Apparatus Having Lift And Tilt Mechanism", U.S. Serial No. 08/991,062 filed December 15, 1997, and is used to rotationally align microelectronic workpieces initially for precise processing. This is particularly important given the fact that the metrology unit can be utilized for measuring precise points in patterned film layers, i.e., accurate positioning of the microelectronic workpiece is important to obtain an accurate reading.

Process Development Embodiment

03612176-070000

An alternate exemplary embodiment of the tool incorporating the present invention is a process development configuration tool 400 illustrated in Figure 4. This tool 400 is directed to developing optimized processes, i.e., for research and development. The tool 400 has a compact layout. The tool configuration allows increased flexibility in process sequence and control. For example, a process engineer might want to measure any combination of incoming seed layer thickness, ECD seed layer deposition results, ECD fill results, and post annealing results. Since the plating solution reservoirs can be much smaller, the user may also quickly and easily interchange chemistries for rapid and low-cost experimentation. The user may want to run split lots with a wide variety of process combinations to determine feasibility of a production process.

The tool 400 includes fewer processing stations than the tool 300 shown in Figure 3. The tool 400 includes two electroplating units 240, an in-line metrology unit 228, an annealing unit 244, a seed layer enhancement unit 232, and two stripping and/or cleaning units 236 for stripping films or backside cleaning as needed. The tool 400 also includes a staging station 248, in this case configured as a wafer pre-aligner 248b.

Process Sequences

FIGURES 5 through 8 illustrate different process sequences which can be employed according to the invention. The process sequences are examples, and the process order can in some cases be rearranged, and process steps can be eliminated or added without departing from the invention.

FIGURE 5 illustrates a first process sequence wherein the microelectronic workpiece is first processed in an ECD unit such as an electroplating unit in a first step 502. Subsequently the workpiece is transferred to a stripping unit and the workpiece is bevel-etched, rinsed and dried in a step 504.

Subsequent to the step 504 the workpiece is transferred to a pre-align station to be accurately positioned, in step 506. The microelectronic workpiece is then transported to the metrology unit in a step 508 and film thickness and/or other parameters are measured. In a step 510 the workpiece is annealed in an annealing unit. The workpiece is thereafter transported to be pre-aligned in a step 512 for accurate reference position. In a step 514 the workpiece is transported to the metrology unit to have parameters such as post annealing film thicknesses measured. The pre-align unit can be incorporated into the metrology unit which would eliminate the need to transport the workpiece to and from a pre-align unit. The metrology data derived from steps 508 and 514 can be used to feed back control information, for example, to the ECD (step 502) for controlling process recipe for subsequent workpieces.

FIGURE 6 illustrates a second sequence of process steps including a first step 602 in which a microelectronic workpiece has a seed layer applied by an ECD reactor. The workpiece is then transported to a rinse and dry station in a step 603 and then to a pre-align station for accurate positioning in a step 604. The workpiece is then transported to a metrology unit in a step 606 for parameter measurements, such as film thickness. In the step 608 the workpiece is then transported to the ECD unit, such as an electroplating unit, to be further processed. In a step 610 the workpiece is then transported to a stripping unit for bevel etch, rinse and dry processing. Subsequently, in a step 612 the microelectronic workpiece is annealed.

The metrology measurement taken in step 606 can be used to control the recipe of the downstream ECD reactor (step 608).

FIGURE 7 illustrates a third sequence of process steps 700 which commences with a pre-align of the workpiece in step 702. The workpiece is then transported to the metrology unit for accurate measuring in a step 704. A barrier layer can be measured in this step. Subsequent to the step 704 the workpiece is transported to an ECD seed layer unit for the deposition of a seed layer onto the workpiece. The workpiece is then transported to a rinse and dry station in a step 707, and then to the pre-align station in a step 708, for accurate reference positioning. The workpiece is then transported back to the metrology unit 710 in a step 710 for accurate measuring of the applied seed layer, for example. After the metrology measurements are taken, the workpiece is transported to an ECD unit,

such as an electroplating unit, in a step 712 and a further processing of the workpiece ensues. Upon completion of the ECD processing the workpiece is transported to a stripping unit for a bevel etch rinse and dry in a step 714. The workpiece is then transported to an annealing unit in a step 716 and the workpiece is annealed.

The metrology measurements taken in steps 704 and 710 can be used to control the recipe in steps 706 and/or 712 as a feed forward or feed back control.

FIGURE 8 illustrates a fourth process sequence of steps 800 which commences at a step 802 with pre-aligning the microelectronic workpiece. The workpiece is then transported to the metrology unit for measurements in a step 804. The workpiece is subsequently transported to an ECD seed layer unit wherein a seed layer is applied to the workpiece in a step 806.

After the seed layer is applied, the workpiece is transported to a bevel etch rinse and dry station in a step 808. The workpiece is then transported back to the pre-align station to be accurately reference positioned in a step 810. After being accurately positioned the workpiece is transported to the metrology unit for further accurate measurements in step 812. The workpiece is thereupon transported to an ECD unit such as an electroplating reactor, wherein further processing of the workpiece ensues in a step 814. After such processing, the workpiece is transported to the bevel etch, rinse and dry station and processed accordingly in a step 816.

The workpiece is then transported to a pre-align station and accurately positioned in a step 818. After being accurately positioned, the workpiece is returned to the metrology unit and in a step 820 is accurately measured. The workpiece is then transported to an annealing unit in a step 822 and is annealed.

After annealing, in a step 824 the workpiece is transported to a pre-align station and is accurately reference positioned. After being accurately positioned, in a step 826 the workpiece is transported back to the metrology unit and accurately measured. In a step 828, the workpiece is transported to a chemical mechanical polishing unit ("CMP") for further processing.

The metrology steps 804, 812, 820 and/or 826 can be utilized to feed forward or feed back control of process recipes or control step sequences.

It should be noted that in FIGURES 5, 6, 7, and 8 the pre-align steps are optional depending on the tool configuration.

Multi-tool Integrated Metrology Systems

FIGURE 9 illustrates an alternate embodiment system 900 for processing a microelectronic workpiece. The system 900 includes a seed layer tool 906, an ECD tool 910, and a chemical mechanical polishing tool 916.

The seed layer tool 906 includes a barrier deposition station 920 and a seed layer deposition station 926. The seed layer tool 920 can also include a cleaning

The ECD tool 910 can be configured as the tools 200, 300, 400, and includes an ECD station 940, such as an electroplating unit 240. The ECD tool 910 can also include a metrology unit 942 for in-line metrology monitoring.

After layers are formed on the microelectronic workpiece, there is a need to planarize the surface of the workpiece. One method of planarizing is chemical mechanical polishing ("CMP"). According to this method, the workpiece is mounted to a carrier or polishing head. The workpiece is placed against a rotating polishing pad. A polishing-slurry, including at least one chemically reactive agent, is supplied to the surface of the pad.

28

A controller 960 can be provided to coordinate the processing of the workpiece as it progresses from the seed layer tool 906 to the ECD tool 910 to the chemical mechanical polishing tool 916. The controller can be a programmable controller. The metrology units 932, 942, 952 can be configured to send measurement data to the controller 960. The controller 960 can control the process recipes or the sequence of process steps on the three tools 906, 910, 916 based on the metrology results from one or more of the metrology units 932, 942, 952.

For example, a metrology result from the metrology unit 932 of the condition of the seed layer applied sequentially by the barrier deposition station 920 and by the seed layer deposition station 926 can be used to control the process recipe in the ECD station 940 and/or the process recipe in the CMP tool 916. In the CMP tool, the polishing time, the rotational speed of the workpiece or pad, the pressure exerted by the pad on the workpiece, and the radial distribution of pad pressure on the workpiece can be process recipe variables which could be controlled or adjusted. If the film to be polished is center thick, the CMP process may be tuned to polish the center faster.

Also as examples, the metrology result from the metrology unit 952 could be used as feedback to control the process recipes on any of the tools 906, 910, 916. The metrology result from the metrology unit 942 could be used as feed back to control the process recipe in the seed layer tool 906 and/or as feed forward to control the process recipe in the chemical mechanical polishing tool 916.

These configurations are exemplary only, and those skilled in the art will recognize a separate metrology station in lieu of units 932, 942 and 952 to effect process parameter control.

Figure 10 illustrates another multi-tool or multi-station system 1000 wherein metrology control can be used. A photoresist deposition station 1010 applies photoresist to a workpiece. The workpiece photoresist layer thickness can then be measured by a methology unit 1020. The workpiece is then exposed in a photoresist exposure station 1026 and then developed in a photoresist develop station 1030. A further metrology unit 1036 can then measure the workpiece layer thickness, or pattern dimension. Through a controller 1040, the metrology unit 1020 can feed forward to control the photoresist develop station 1026 or feed back to control the photoresist deposition station 1010. The further metrology unit 1036 can feed back to control the photoresist develop station 1030, via the controller 1040. The controller can be a programmable controller.

COMPONENTS

Electroplating Units

The electroplating units 240, 940 of the tools 200, 300, 400, 910 each include a plating reactor such as described in "Improved Anode Assembly For Electroplating Apparatus", U.S. Serial No. 09/112,300 filed July 9, 1998, or an adjustable plating reactor as described in "Workpiece Processor Having Processing Chamber With

Improved Processing Fluid Flow", PCT/US00/10210 filed April 13, 2000 or "System For Electrochemically Processing A Workpiece", PCT/US00/10120 filed April 13, 2000, WO 00/14308 March 16, 2000, all herein incorporated by reference. Alternate reactor types are described in WO 00/20663, published April 13, 2000; WO 99/10566, published March 4, 1999; WO 99/54527, published October 28, 1999; WO 99/54920, published October 28, 1999; and WO 99/25904, published May 27, 1999, and are encompassed by the invention.

Preferably, the plating reactor is an adjustable reactor (as referenced above) that includes a processing container for providing a flow of a processing fluid during immersion processing of at least one surface of a microelectronic workpiece. The processing container comprises a principal fluid flow chamber providing a flow of processing fluid to at least one surface of the microelectronic workpiece. The fluid flow inlets are arranged and directed to provide vertical and radial fluid flow components that combine to generate a substantially uniform normal flow component radially across the surface of the microelectronic workpiece.

The reactor comprises a reactor head including a microelectronic workpiece support that has one or more electrical contacts positioned to make electrical contact with the microelectronic workpiece. A plurality of anodes are disposed at different elevations in the principal fluid flow chamber so as to place them at different distances from a microelectronic workpiece under process. One or more of the plurality of anodes may be in close proximity to the microelectronic workpiece under

process. Still further, one or more of the plurality of anodes may be a virtual anode.

The anodes used in the electroplating reactor can be placed in close proximity to the surface of the microelectronic workpiece to thereby provide substantial control over local electrical field/current density parameters used in the electroplating process.

This substantial degree of control over the electrical parameters allows the reactor to be readily adapted to meet a wide range of electroplating requirements (e.g., seed layer thickness, seed layer type, electroplated material, electrolyte bath properties, etc.) without a corresponding change in the reactor hardware. Rather, adaptations can be implemented by altering the electrical parameters used in the electroplating process through, for example, software control of the power provided to the anodes.

Advantage can be taken of this increased control to achieve greater uniformity of the resulting electroplated film. Such control is exercised, for example, by placing the electroplating power provided to the individual anodes under the control of a programmable controller or the like. Adjustments to the electroplating power can thus be made subject to software control based on a metrology-based signal, based on seed layer thickness, for example.

It will be recognized that the particular currents that are to be provided to the anodes depends upon numerous factors including, but not necessarily limited to, the desired thickness and material of the electroplated film, the thickness and material of the initial seed layer, the distances between anodes and the surface of the microelectronic workpiece, electrolyte bath properties, etc.

Although the aforementioned adjustable reactor controls electroplating power to individual anodes, other methods of controlling electroplating film uniformity in response to metrology results are encompassed by the invention including adjusting current density, using current thieves or controlling workpiece rotation and/or fluid flow.

As an alternative to the electroplating reactors, electroless plating reactors (as described below) can be utilized in some applications.

Stripping Units

An example of the stripping unit 236 is described in "Micro-Environment For Processing A Workpiece", PCT/US99/05676 filed March 15, 1999 and/or in "Selective Treatment Of A Microelectronic Workpiece", PCT/US99/05674 filed March 15, 1999 herein incorporated by reference. The "stripping units" are multifunctional processing capsules which can perform cleaning, stripping, bevel etching, rinsing and drying.

An apparatus for processing a microelectronic workpiece in a "micro-environment" is set forth in the aforementioned PCT applications. The apparatus includes a rotor motor and a microelectronic workpiece housing. The microelectronic workpiece housing is connected-to-be-rotated by the rotor motor. The microelectronic workpiece housing further defines a substantially closed processing chamber therein in which one or more processing fluids are distributed

The microelectronic workpiece housing includes an upper chamber member having a fluid inlet opening and a lower chamber member having a fluid inlet opening. The upper chamber member and the lower chamber member are joined to one another to form the substantially closed processing chamber. The processing chamber generally conforms to the shape of the microelectronic workpiece and includes at least one fluid outlet disposed at a peripheral region thereof. At least one microelectronic workpiece support is provided. The support is adapted to support a microelectronic workpiece in the substantially closed processing chamber in a position to allow centripetal acceleration distribution of a fluid supplied through the inlet opening of the upper chamber member across at least an upper face of the microelectronic workpiece when the microelectronic workpiece housing is rotated. The wafer is further positioned by the support to allow centripetal acceleration distribution of a fluid supplied through the inlet opening of the lower chamber member across at least a lower face of the microelectronic workpiece during the rotation. The at least one fluid outlet is positioned to allow extraction of fluid in the processing chamber through the action of centripetal acceleration.

34

the outer margin. Thus, a non-uniform seed layer, for example, can be stripped from the workpiece.

Seed Layer Enhancement Units

An example of the seed layer enhancement unit 232 is described in "Apparatus And Method For Electrolytically Depositing Copper On A Semiconductor Workpiece", PCT/US99/06306, filed March 22, 1999 and herein incorporated by reference. The seed layer enhancement unit 232 can be embodied as an adjustable plating reactor as described in "Workpiece Processor Having Processing Chamber With Improved Processing Fluid Flow", PCT/US00/10210 filed April 13, 2000 or "System For Electrochemically Processing A Workpiece", PCT/US00/10120 filed April 13, 2000 herein incorporated by reference.

In accordance with a specific embodiment of the process, an ultra-thin adhesion layer, formed by physical vapor deposition (PVD), is enhanced by subjecting the semiconductor microelectronic workpiece to an electrochemical copper deposition process in which an alkaline bath having a complexing agent is employed. The copper complexing agent may be at least one complexing agent selected from a group consisting of EDTA, ED, and a polycarboxylic acid such as citric acid or salts thereof. The alkaline electrolytic copper bath is used to enhance the ultra-thin copper adhesion layer which has been deposited on a barrier layer using a deposition process such as PVD. The enhanced copper seed layer provides

an excellent conformal copper coating that allows trenches and vias to be subsequently filled with a copper layer having good uniformity using electrochemical deposition techniques.

Alternately, the seed layer enhancement units 232 can be embodied as electroless plating reactors as described below.

Electroless Unit

Another process for depositing a layer (such as copper) onto a microelectronic workpiece is known as "electroless" plating. Unlike an electroplating reactor, electroless plating does not conduct external electrical power to the surface of a microelectronic workpiece. A catalytic material is used to effect plating of the material on the microelectronic workpiece. Electroless plating reactors and corresponding processes are disclosed in WO 00/03072, published January 20, 2000; and U.S. Patents 5,500,315; 5,389,496; and 5,139,818, all incorporated herein by reference. Electroless plating can be used instead of electroplating, or can be used as a seed layer enhancement step.

Annealing Units

The annealing units 244 can be as described in "Method And Apparatus For Tuning Multiple Electrodes Used In A Reactor For Electrochemically Processing A Microelectronic Workpiece", U.S. Serial No. 60/206,663, filed May 24, 2000, or as in

"Method And Apparatus For Low Temperature Annealing Of Metallization Micro-Structures In The Production Of A Microelectronic Device", PCT/US99/02504, filed February 2, 1999; or as in "Method And Apparatus For Processing A Microelectronic Workpiece Including An Apparatus And Method For Executing A Processing Step At An Elevated Temperature", U.S. Serial No. 09/501,002, filed February 9, 2000; herein incorporated by reference. The annealing units 244 can include a thermal reactor that is adapted for rapid thermal processing (RTP).

The microelectronic workpieces are transferred between the processing stations and the annealing units 244 using the transport unit 256 that is disposed for linear movement along the central track.

Metrology Unit

Each of the metrology units 228, 932, 942, 952, 1020, 1036 can be a four-point probe style metrology tool. The metrology units can use sheet resistance or capacitance to determine layer thickness. Alternately, the metrology units can use optical or thermal reference methods. According to an exemplary embodiment, the metrology units use a laser based non-constant metrology system wherein the laser induces an acoustic response in the measured film and the acoustic response is related to film thickness. This is known as impulsive stimulated thermal scattering (ISTS). One such system is manufactured by Philips Analytical under the model

Input/Output Station

Non-compliance Station

38

back to the PVD seed layer deposition station for reestablishing a seed layer on the microelectronic workpiece substrates.

Linear Robot System

The linear robot system can be as described in "Semiconductor Processing Apparatus Having Linear Conveyor System", PCT/US98/00132 filed January 6, 1998; or "Semiconductor Processing Apparatus Having Lift And Tilt Mechanism", PCT/US98/00076 filed January 5, 1998; or "Robots For Microelectronic Workpiece Handling", PCT/US99/15567 filed July 9, 1999, all herein incorporated by reference.

Chemical Mechanical Polishing Station

Chemical mechanical polishing tools are disclosed in WO 00/26609, published May 11, 2000, and U.S. Patent 5,738,574, herein incorporated by reference.

From the foregoing, it will be observed that numerous variations and modifications may be effected without departing from the spirit and scope of the invention. It is to be understood that no limitation with respect to the specific apparatus illustrated herein is intended or should be inferred. It is, of course, intended to cover by the appended claims all such modifications as fall within the scope of the claims.

WE CLAIM:

1. A processing apparatus for processing a microelectronic workpiece, comprising:
an in-line metrology unit having a space for receiving a microelectronic workpiece for measuring a condition of a first layer on said microelectronic workpiece and generating a condition signal;
a control, signal-connected to said metrology unit;
a process unit providing a space to receive said microelectronic workpiece and performing a process that is controlled by said control;
wherein said condition signal from said metrology unit to said control influences said process.
2. The apparatus according to claim 1, further comprising a non-compliance unit, and a microelectronic workpiece transport signal-connected to said control, wherein said condition signal from metrology unit influences said control to cause said microelectronic workpiece transport to transfer the microelectronic workpiece to said non-compliance unit.

- 41

- 42

11. The apparatus according to claim 1, wherein said process unit comprises a photoresist exposure tool, and said first layer comprises a photoresist layer on said workpiece just prior to exposure by said photoresist exposure tool, and said condition to be measured is the photoresist thickness.

12. The apparatus according to claim 1, wherein said process unit comprises a photoresist exposure tool, and said first layer comprises a photoresist layer on said workpiece just after develop by a subsequent develop tool, and said condition to be measured is the pattern dimension.

13. A method of processing a microelectronic workpiece, comprising the steps of:

providing two processing tools each of which further processes a

microelectronic workpiece in a preselected process;

using an in-line metrology unit, determining a condition of a layer on said

microelectronic workpiece at one of the tools; and

in response to a signal from the metrology unit, modifying a process

parameter in the respective other tool.

- 05512176 070300
14. The method according to claim 13, comprising the further step of providing a third process tool in a preselected process order with respect to the two consecutive processing tools, and modifying process parameters in said third processing tool in response to said signal from said metrology unit.
 15. The method according to claim 13, wherein said microelectronic workpiece is processed first in said one processing tool and subsequently in said respective other processing tool.
 16. The method according to claim 13, wherein said microelectronic workpiece is processed first in said respective other processing tool and subsequently in said one processing tool.
 17. A processing apparatus for processing a microelectronic workpiece, comprising:
an in-line metrology unit having a space for receiving a microelectronic workpiece and configured to generate condition data in response to a measured condition on said microelectronic workpiece;
a processing unit providing a space to receive a microelectronic workpiece to process a layer on said microelectronic workpiece;

18. The apparatus according to claim 17, wherein said processing unit comprises an electrochemical deposition unit.
19. The apparatus according to claim 17, wherein said processing unit comprises a chemical mechanical polishing unit.
20. The apparatus according to claim 17, wherein said measured condition comprises a seed layer thickness.
21. The apparatus according to claim 17, wherein said measured condition comprises a finished electroplated material thickness.
22. The apparatus according to claim 17, wherein said measured condition comprises a finished chemical mechanical polished thickness.
23. A processing apparatus for processing a microelectronic workpiece, comprising:

a seed layer unit providing a space to receive said microelectronic workpiece to effect deposition of a seed layer on said microelectronic workpiece;

an electrochemical deposition unit providing a space to receive said microelectronic workpiece having said seed layer, to deposit a process layer thereon;

a chemical mechanical polishing tool providing a space to receive said microelectronic workpiece and having a polishing mechanism for removing material from said process layer to form a polished layer;

a metrology unit having a space for receiving a microelectronic workpiece, and arranged to measure a layer thickness on said microelectronic workpiece and to transmit a condition signal;

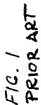
a control unit, signal-connected to said metrology unit; wherein said condition signal from said metrology unit causes said control to adjust process parameters in at least one of said seed layer unit, said electrochemical deposition unit, and said chemical mechanical polishing tool.

24. The apparatus according to claim 23, wherein said metrology unit measured layer thickness is a thickness of said seed layer.

25. The apparatus according to claim 23, wherein said metrology unit measured layer thickness is a thickness of said process layer.
26. The apparatus according to claim 23, wherein said metrology unit measured layer thickness is a thickness of said polished layer.

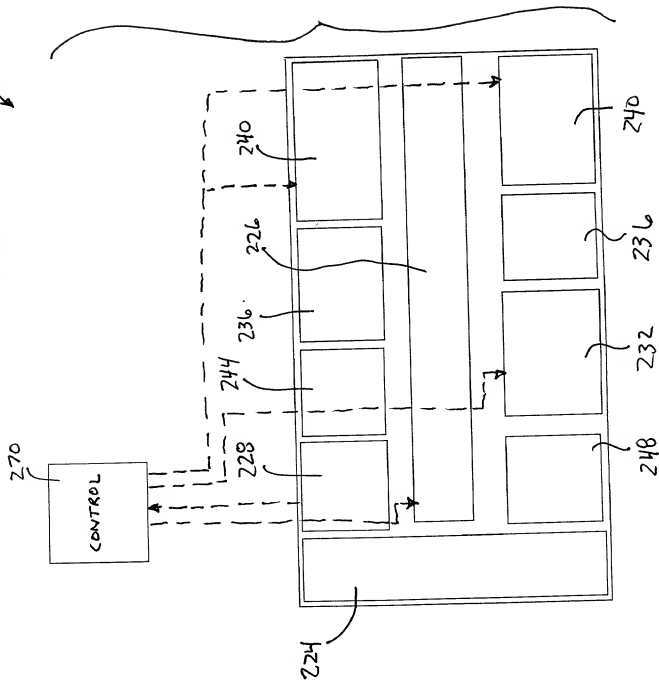
ABSTRACT OF THE DISCLOSURE

A processing apparatus for processing a microelectronic workpiece includes a metrology unit and a control, signal-connected to the metrology unit. The control can modify a process recipe or a process sequence of the processing apparatus based on a feed forward or a feed back signal from the metrology unit. A seed layer deposition tool, a process layer electrochemical deposition tool, and a chemical mechanical polishing tool, arranged for sequential processing of a workpiece, can be controlled as an integrated system using one or more metrology units. A metrology unit can be located at each tool to measure workpiece parameters. Each of the metrology units can be used as a feed forward control and/or a feed back control at each of the tools.



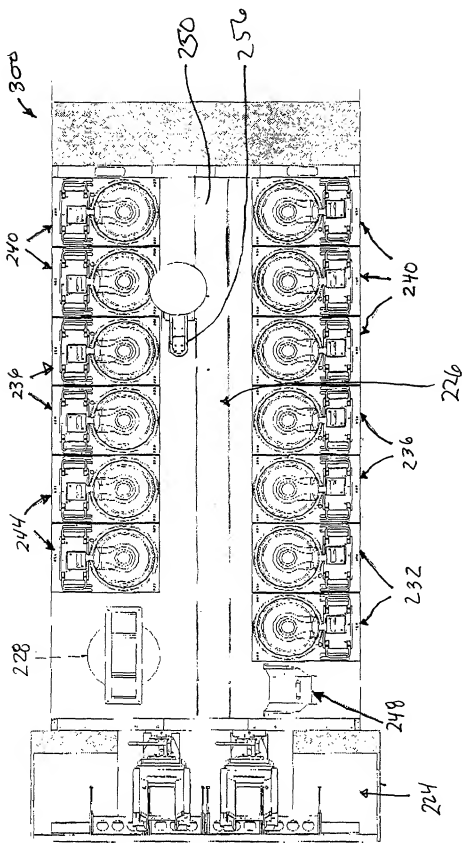
008020.92121960

200



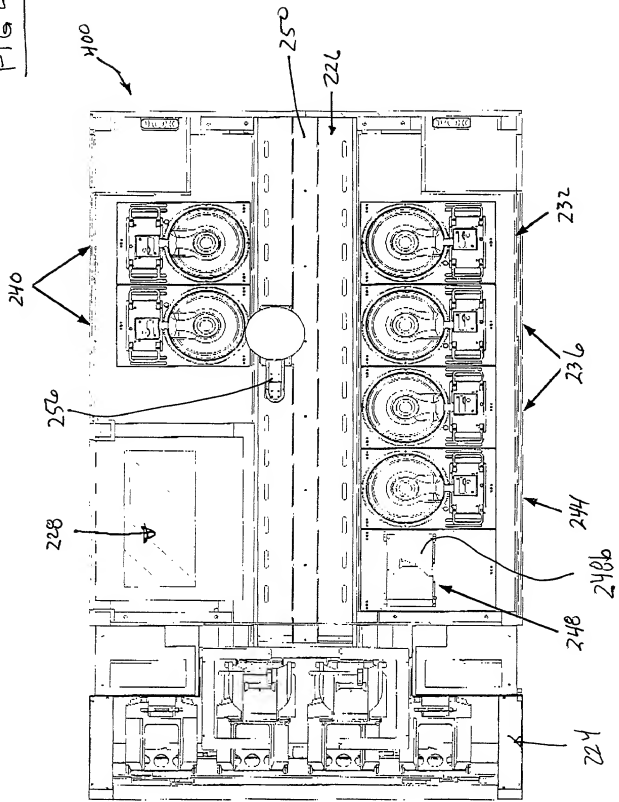
3/10

FIG 3



4/10

FIG 4



09612176.070800

5/10

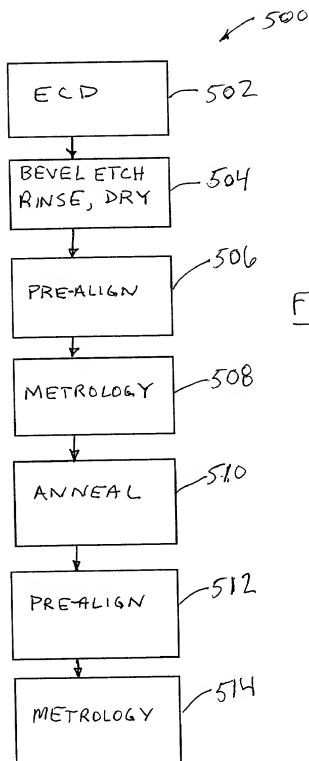


FIG 5

7/10

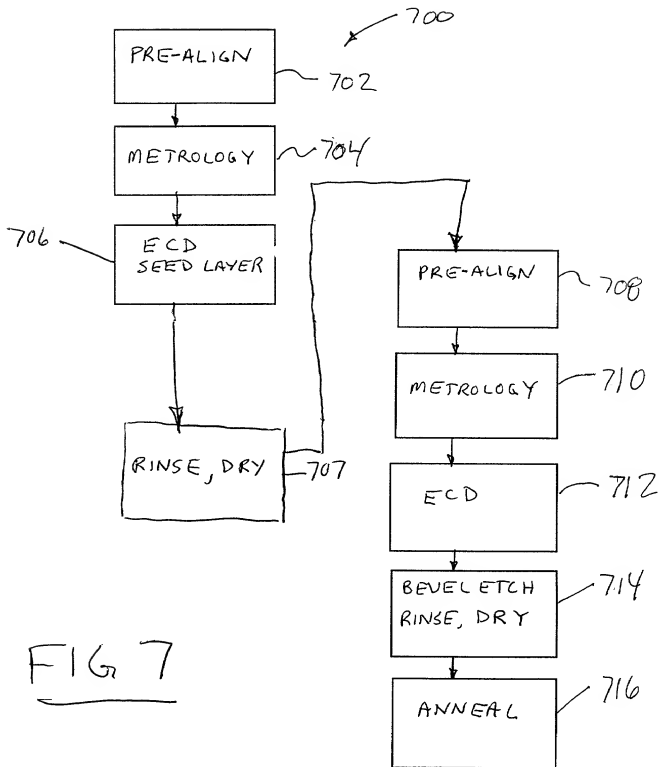
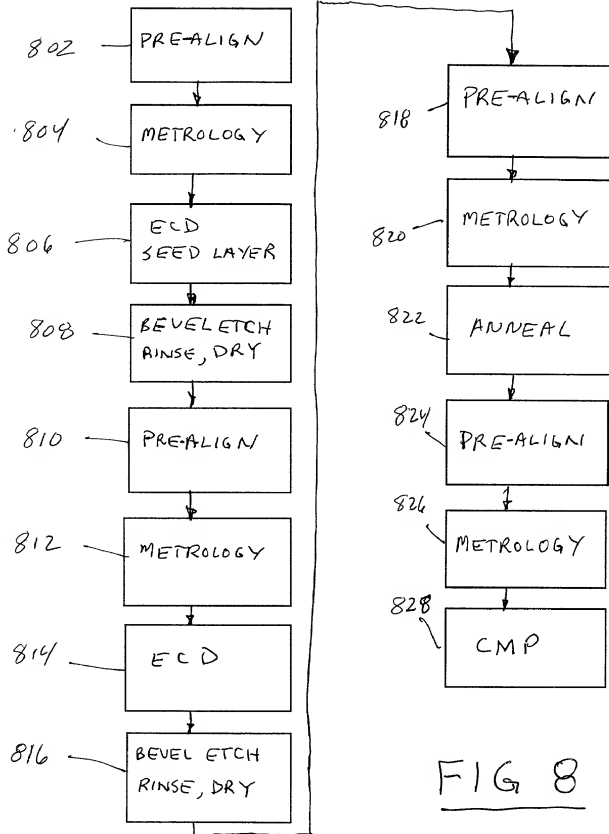


FIG 7

008070.9721960

8/10

800

FIG 8

9/10

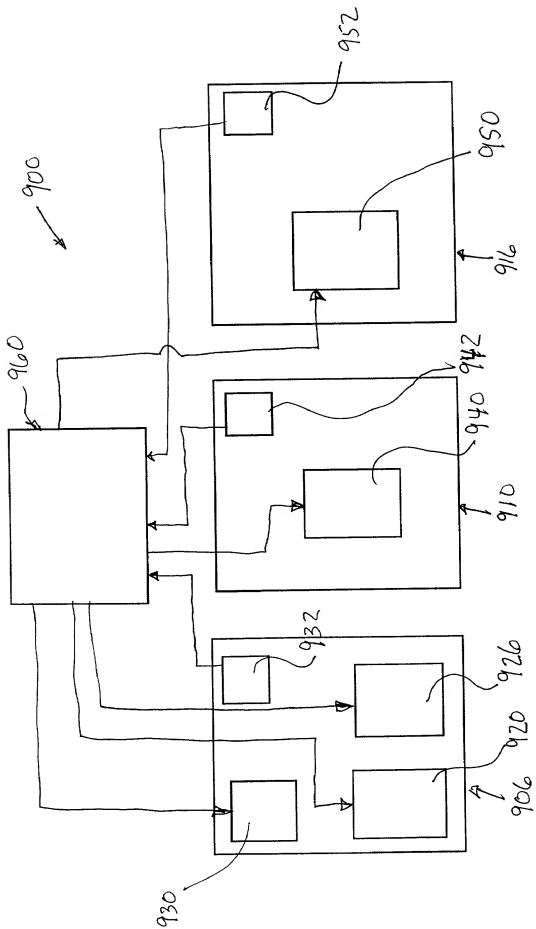


FIG 9

... 10/10

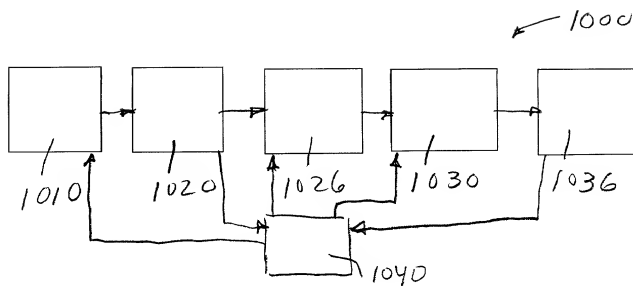


FIG. 10

DECLARATION FOR UTILITY OR DESIGN PATENT APPLICATION (37 CFR 1.63)		Attorney Docket No.: SEM4492P1360US
		First Named Inventor: Thomas L. Ritzdorf
		COMPLETE IF KNOWN
		Application Number:
		Filing Date:
		Group Art Unit:
<input checked="" type="checkbox"/> Declaration Submitted With Initial Filing <input type="checkbox"/> Declaration Submitted After Initial Filing (surcharge (37 CFR 1.16(a)) required)		Examiner Name:

As a below-named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name.

I believe I am the original, first, and sole inventor (if only one name is listed) or an original, first, and joint inventor (if plural names are listed) of the subject matter which is claimed and for which a patent is sought on the invention entitled: **Apparatus And Method For Processing A Microelectronic Workpiece Using Metrology**, the specification of which:

- ☒ is attached hereto; or
- ☐ was filed on _____ as Application Serial No. _____ and was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose all information to the Patent and Trademark Office known to me to be material to patentability of this application, as defined in 37 CFR. 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT International application which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or of any PCT International application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application Numbers	Country	Foreign Filing Date	Priority Not Claimed	Certified Copy Attached?	
				YES	NO
			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

- ☐ Additional foreign application numbers are listed on a supplemental priority data sheet (PTO/SB/02B) attached hereto.

I hereby claim the benefit under 35 U.S.C. 119 (e) of any United States application(s) listed below.

Application Number(s)	Filing Date	<input type="checkbox"/> Additional provisional application numbers are listed on a supplemental priority data sheet (PTO/SB/02B) attached hereto.

I hereby claim the benefit under 35 U.S.C. 120 of any United States application(s), or 365(c) of any PCT International application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. 112, I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application.

U.S. Parent Application or PCT Parent Number	Parent Filing Date	Parent Patent Number (if applicable)

☐ Additional U.S. or PCT International application numbers are listed on a supplemental priority data sheet (PTO/SB/02B) attached hereto.

As a named inventor, I hereby appoint the following registered practitioner(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

Lawrence J. Chapa	Reg. No. 39,135	Paul M. Odell	Reg. No. 28,332
Randall T. Erickson	Reg. No. 33,872	Robert B. Polit	Reg. No. 33,993
Stephen D. Geimer	Reg. No. 28,846	Elaine M. Ramesh	Reg. No. 43,032
Allen J. Hoover	Reg. No. 24,103	Keith V. Rockey	Reg. No. 24,713
Martin L. Katz	Reg. No. 25,011	John F. Rollins	Reg. No. 38,013
Kathleen A. Lyons	Reg. No. 31,852	Thomas I. Ross	Reg. No. 29,275
John P. Milnamow	Reg. No. 20,635	Joel E. Siegel	Reg. No. 25,440
Lisa V. Mueller	Reg. No. 38,978	Paul M. Vargo	Reg. No. 29,116

whose mailing address for this application is: ROCKEY, MILNAMOW & KATZ, LTD.

Two Prudential Plaza - Suite 4700
180 North Stetson Avenue
Chicago, Illinois 60601
Telephone: (312) 616-5400
Facsimile: (312) 616-5460

I hereby declare that all statements made herein of my knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Name of Sole or First Inventor:	Thomas L. Ritzdorf	
Citizenship:	USA	
Residence:	3130 Parkwood Lane, Bigfork, MT 59911	
Post Office Address (if different):	same as residence	
Signature:		Date:

Name of Additional Inventor, if any:	Steve L. Eudy	
Citizenship:	USA	
Residence:	1185 Hwy. 209, Bigfork, MT 59911	
Post Office Address (if different):	same as residence	
Signature:		Date:

Name of Additional Inventor, if any:	Gregory J. Wilson	
Citizenship:	USA	
Residence:	427 6th Ave. East, Kalispell, MT. 59901	
Post Office Address (if different):	same as residence	
Signature:		Date:

Name of Additional Inventor, if any:	Paul R. McHugh	
Citizenship:	USA	
Residence:	1912 Darlington Drive, Kalispell, MT 59901	
Post Office Address (if different):	same as residence	
Signature:		Date:

Name of Additional Inventor, if any:		
Citizenship:		
Residence:		
Post Office Address (if different):		
Signature:		Date: